

Input Queue 3201				Output Queue 3202			
Port	R/W	Address	Data	Valid	Port	Data	
3	R	1000		1	3	11...0	
4	W	4000	10...1	0			
3	W	1000	111...0	0			
3	R	2000		1	3	101...1	
					⋮		

Fig 32

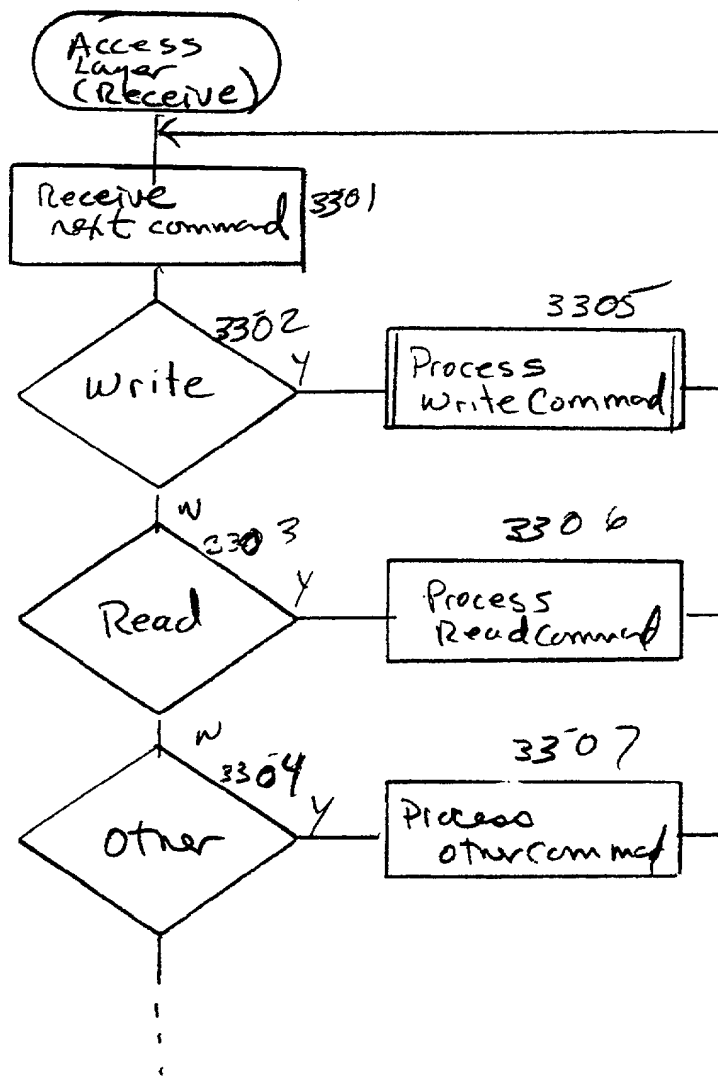


Fig 33

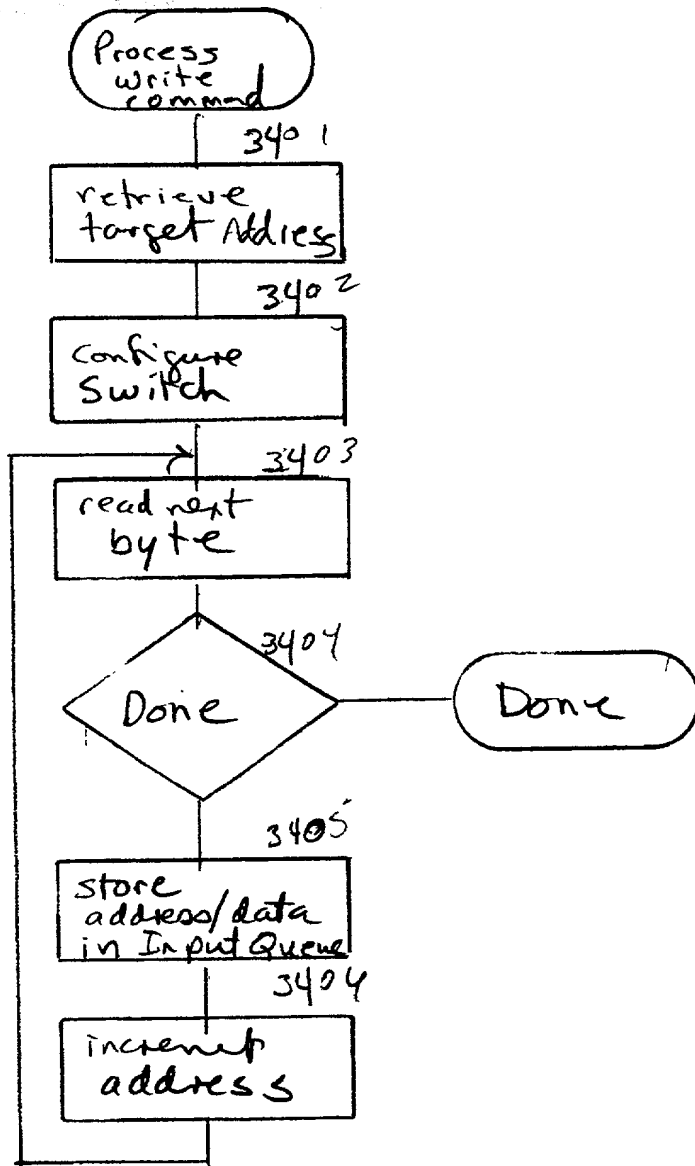


Fig 34

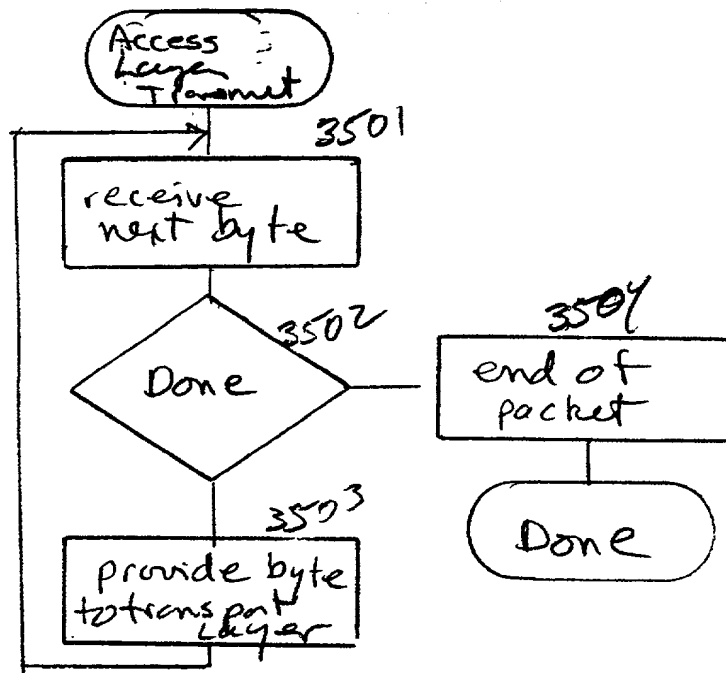
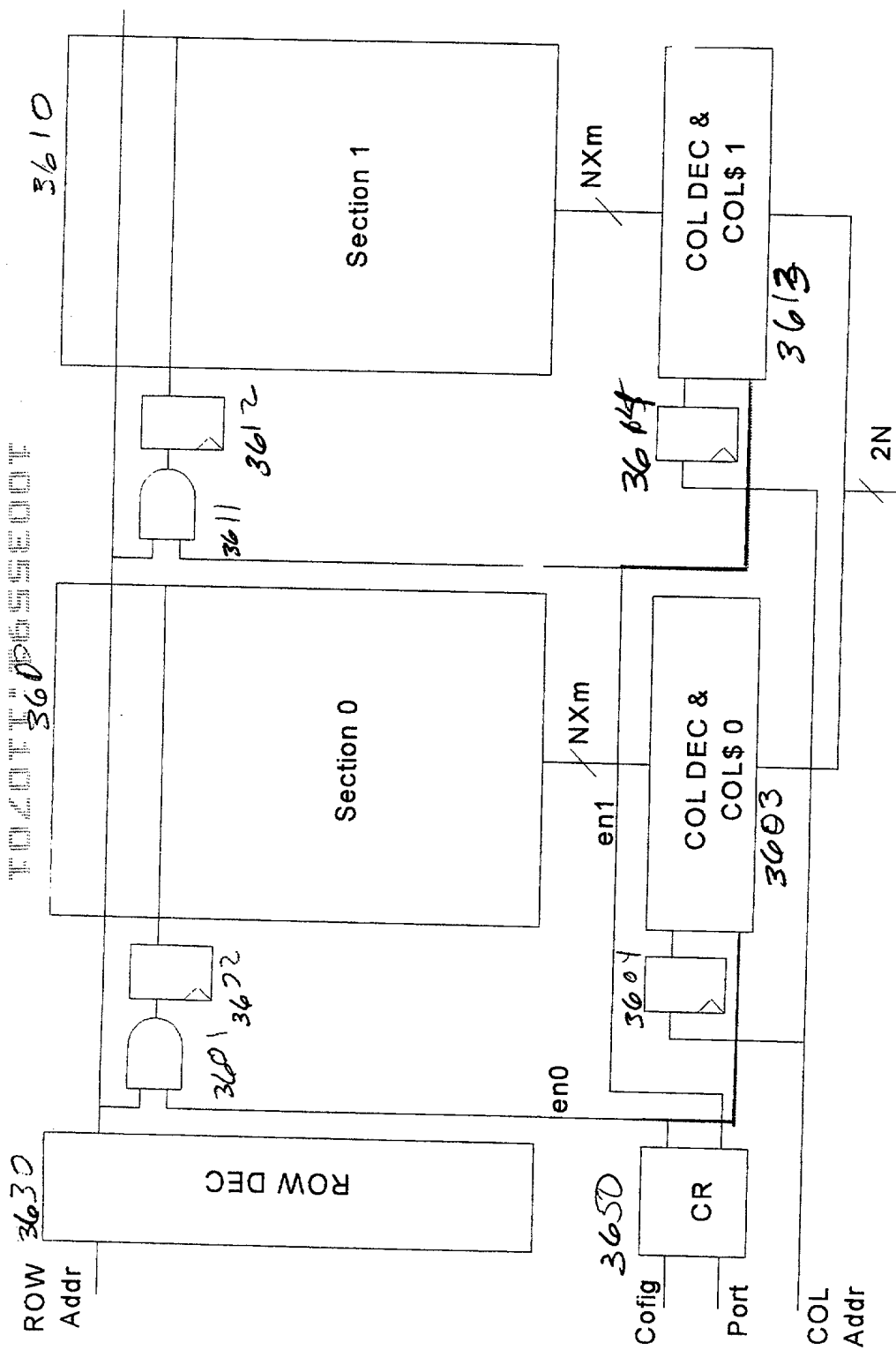
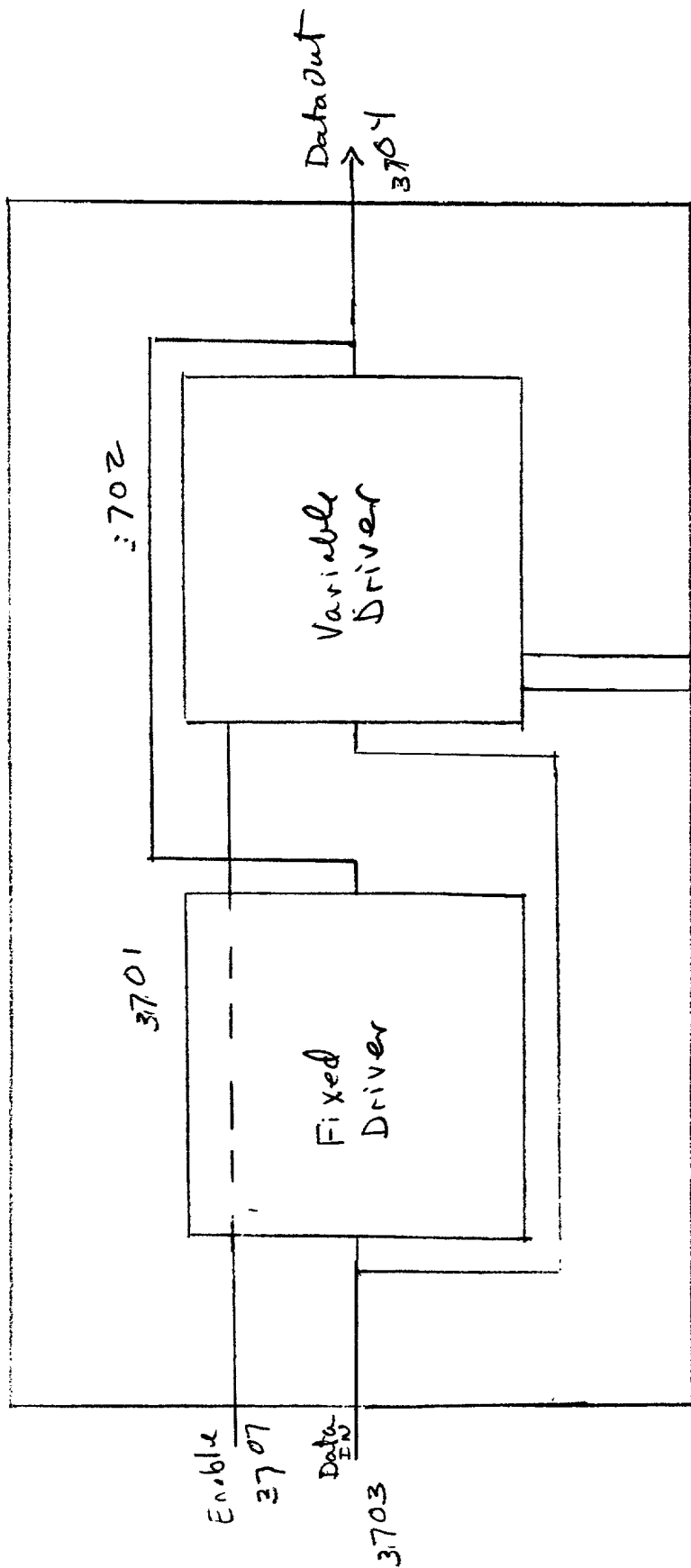


Fig 35



F. 36

Line Driver 3700



Variable Driver

$$\begin{cases} RD^+ \wedge DataIn = \text{pull down} \\ RD^- \wedge DataIn = \text{pull up} \end{cases}$$

Fig 37A

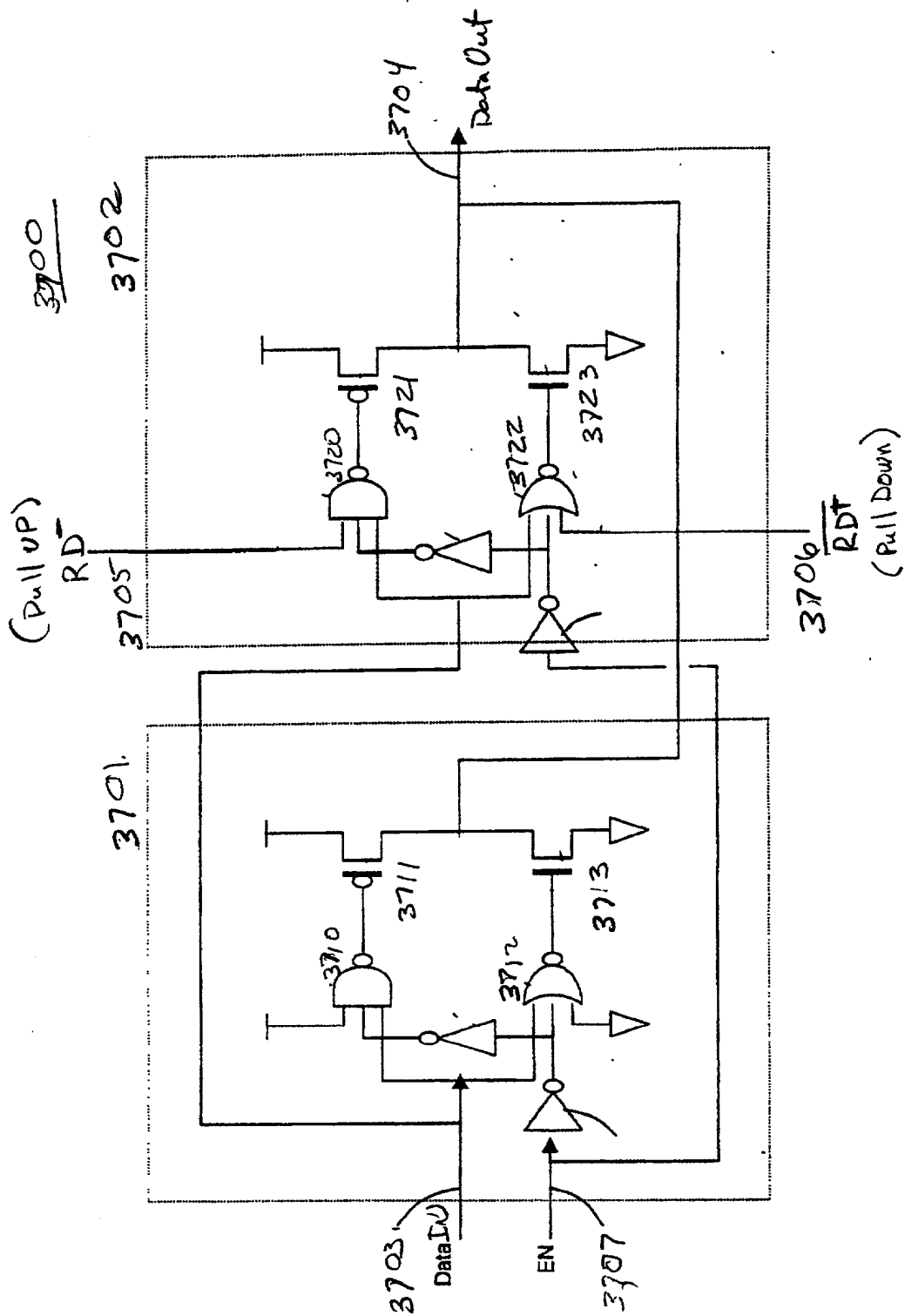


Fig 37B

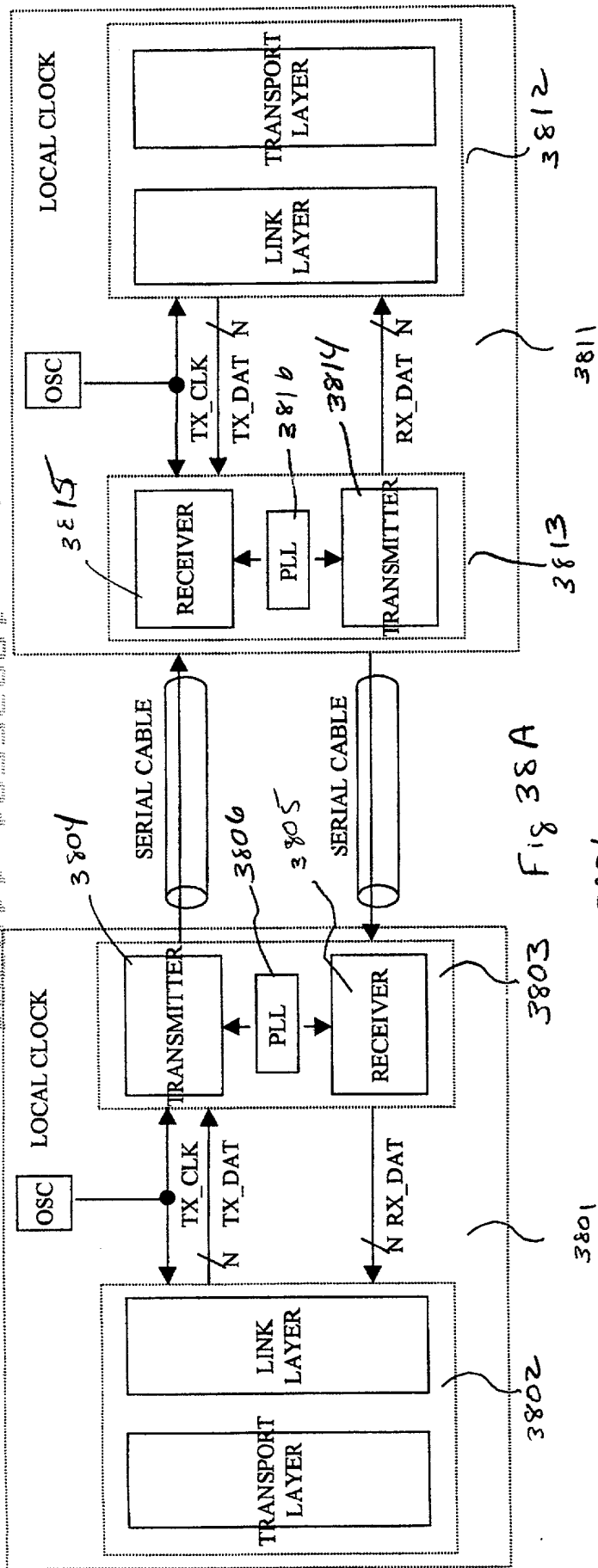


FIG. 38A

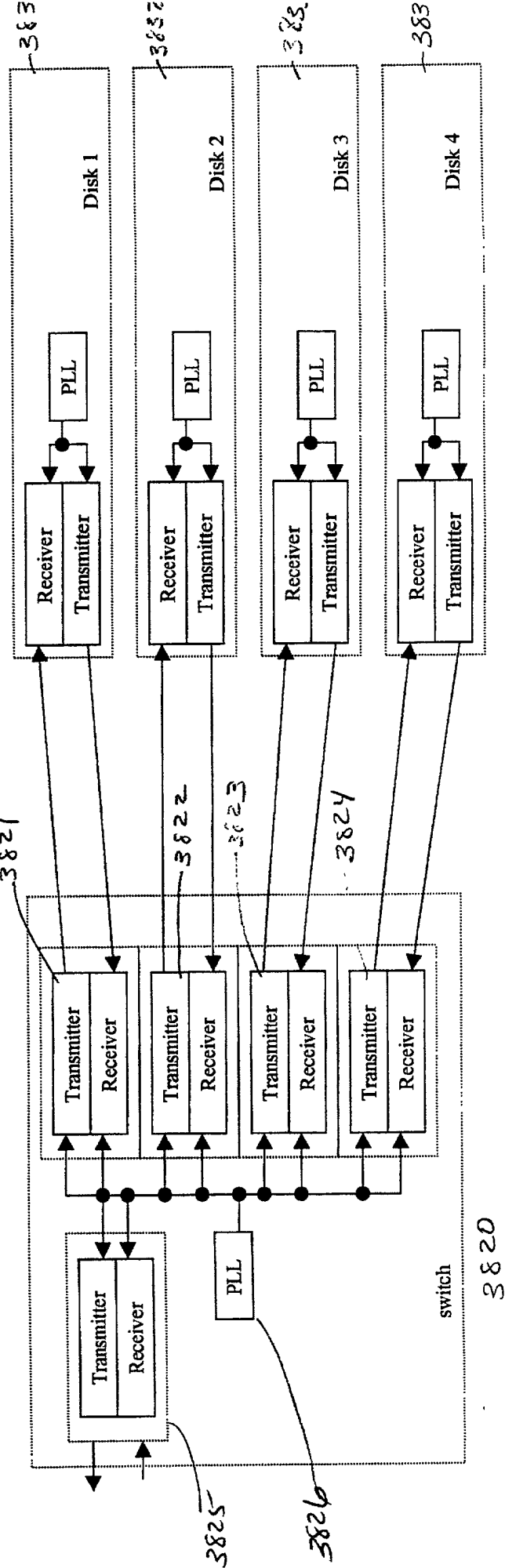


FIG. 38B

FIG. 39A

3920

3921

3910

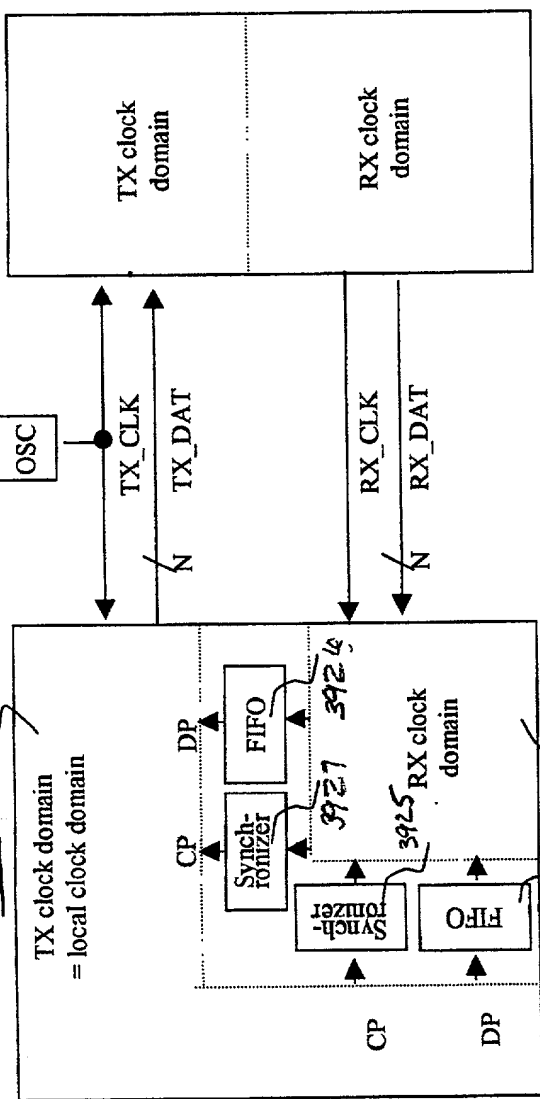


Fig 39A

3940

3950

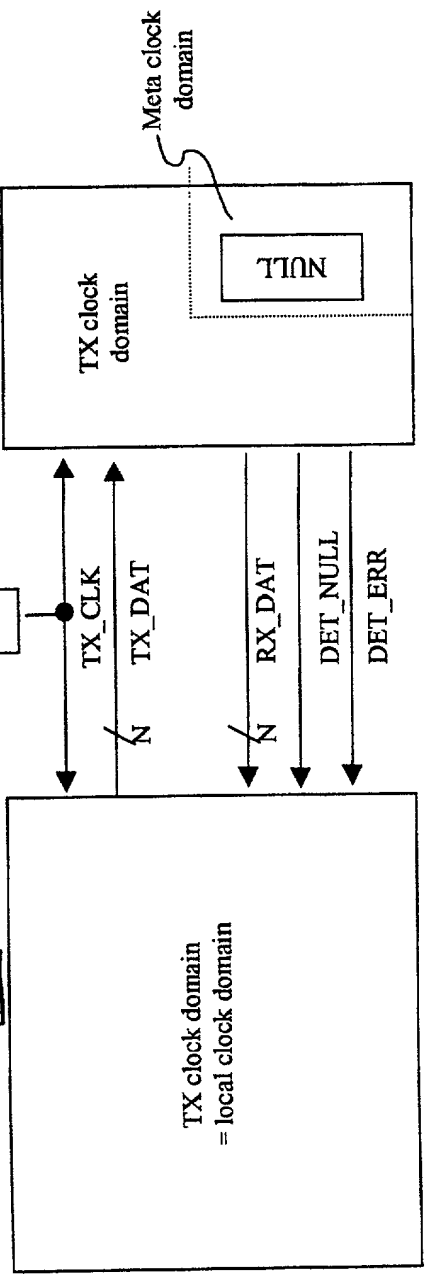


Fig 39B

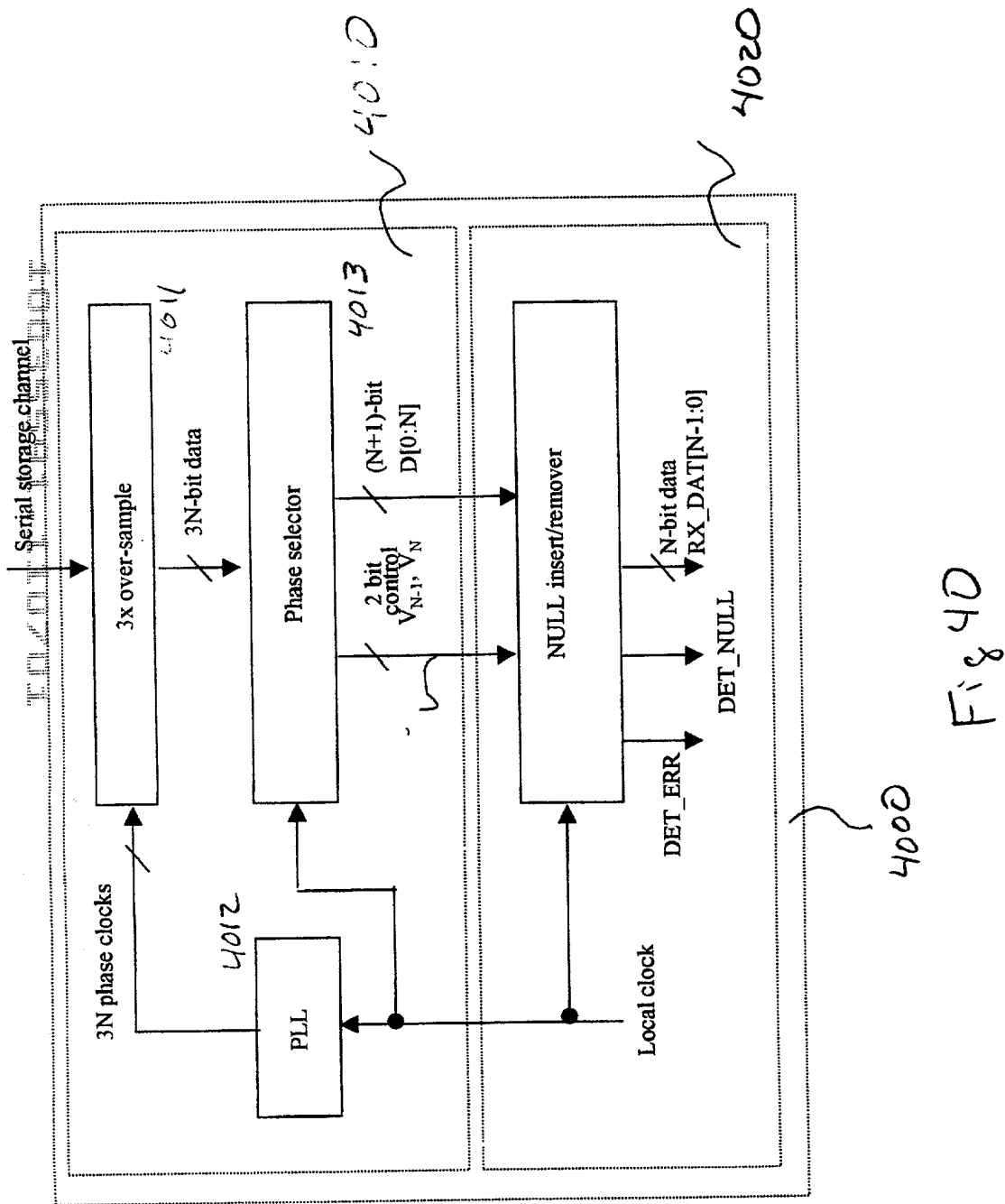


Fig 40

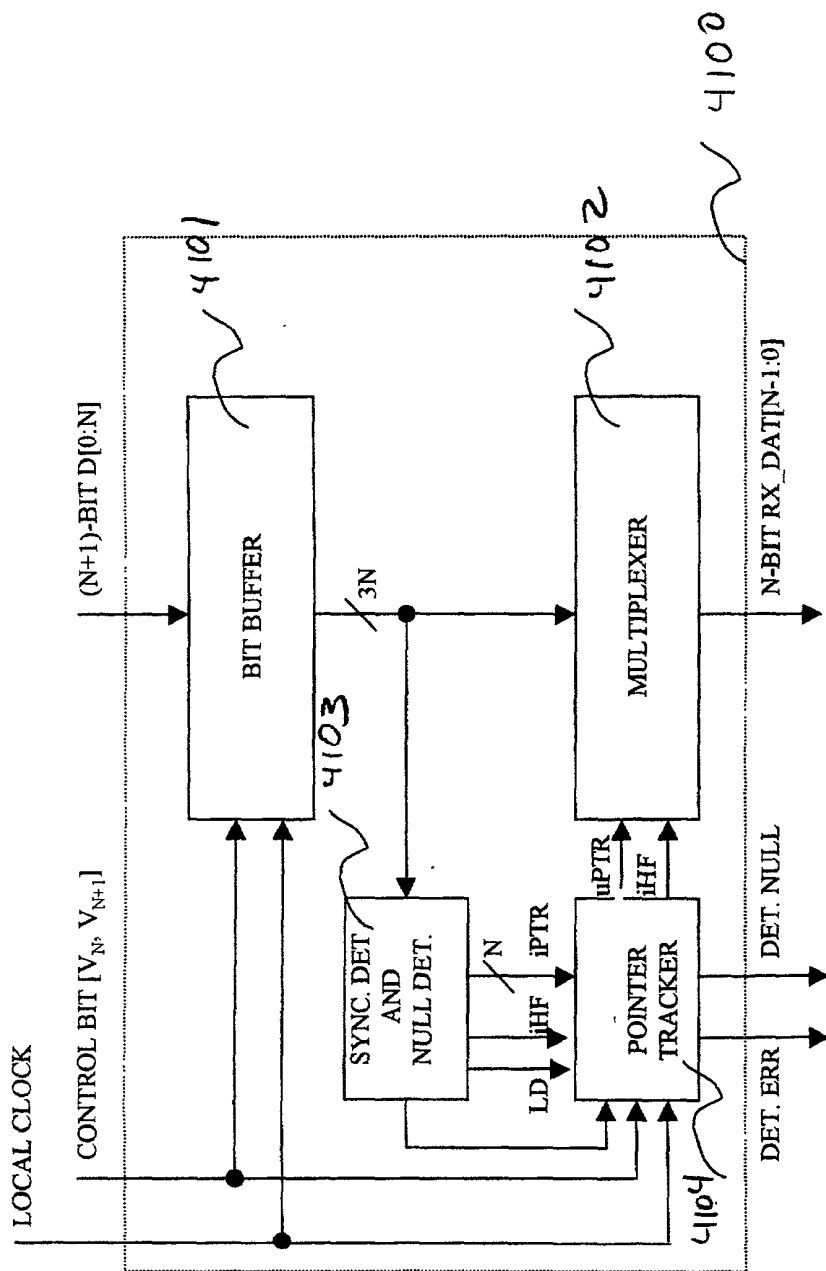


Fig 41

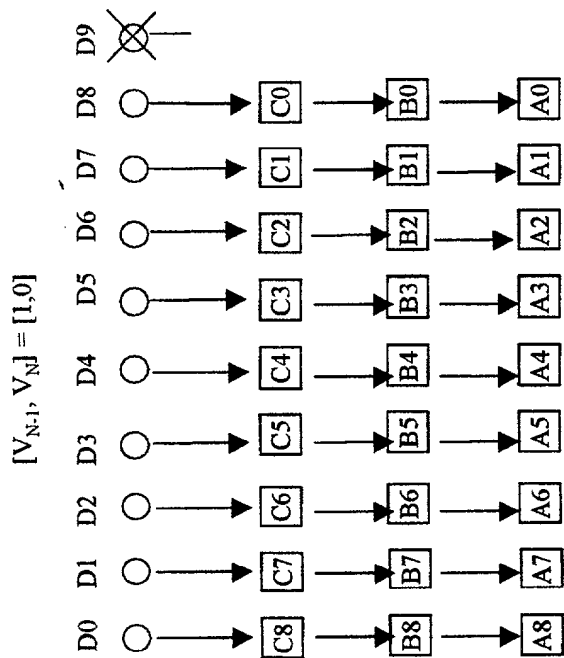


Fig 42A

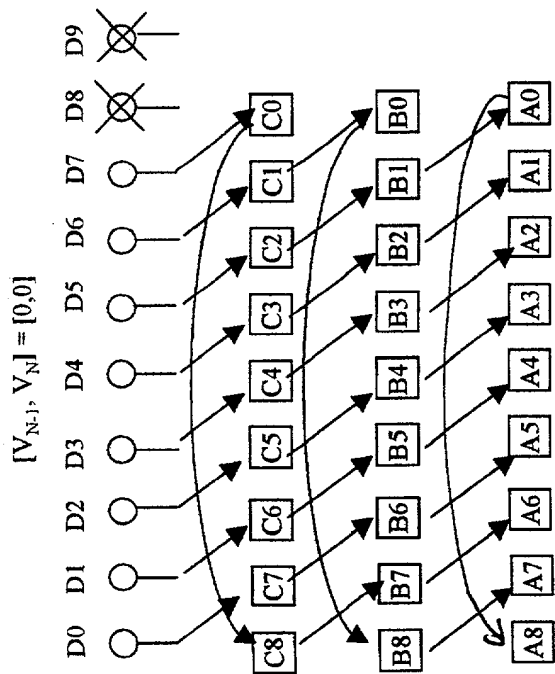


Fig 42B

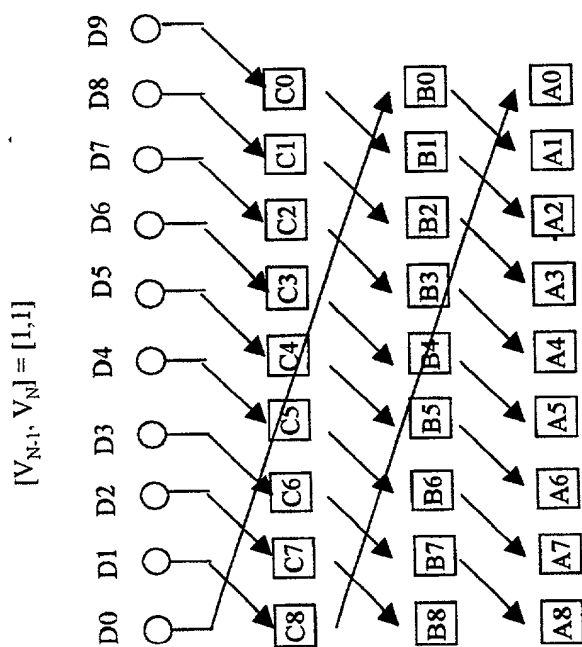
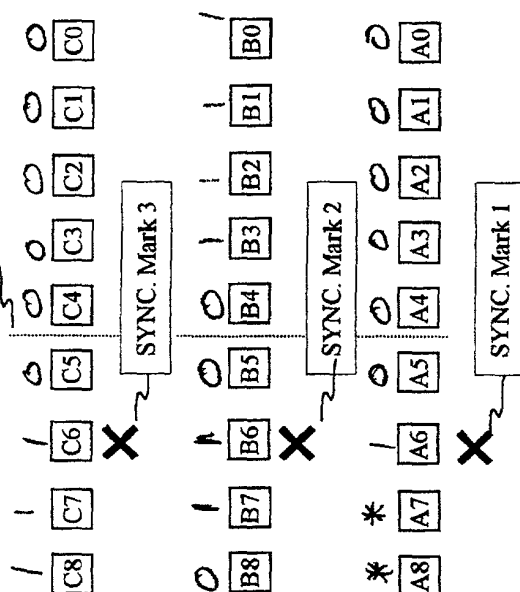


Fig 42c

4301

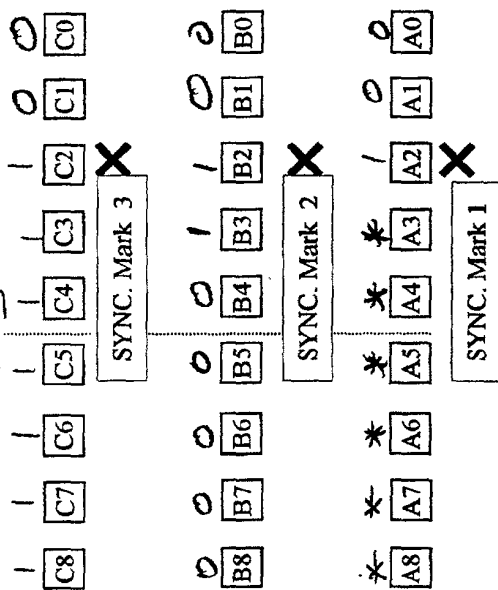
Half line


$$\text{LD} \approx 1, \text{iHF} = 0, \text{iPTR} = "001000000"$$

SYNC. Mark

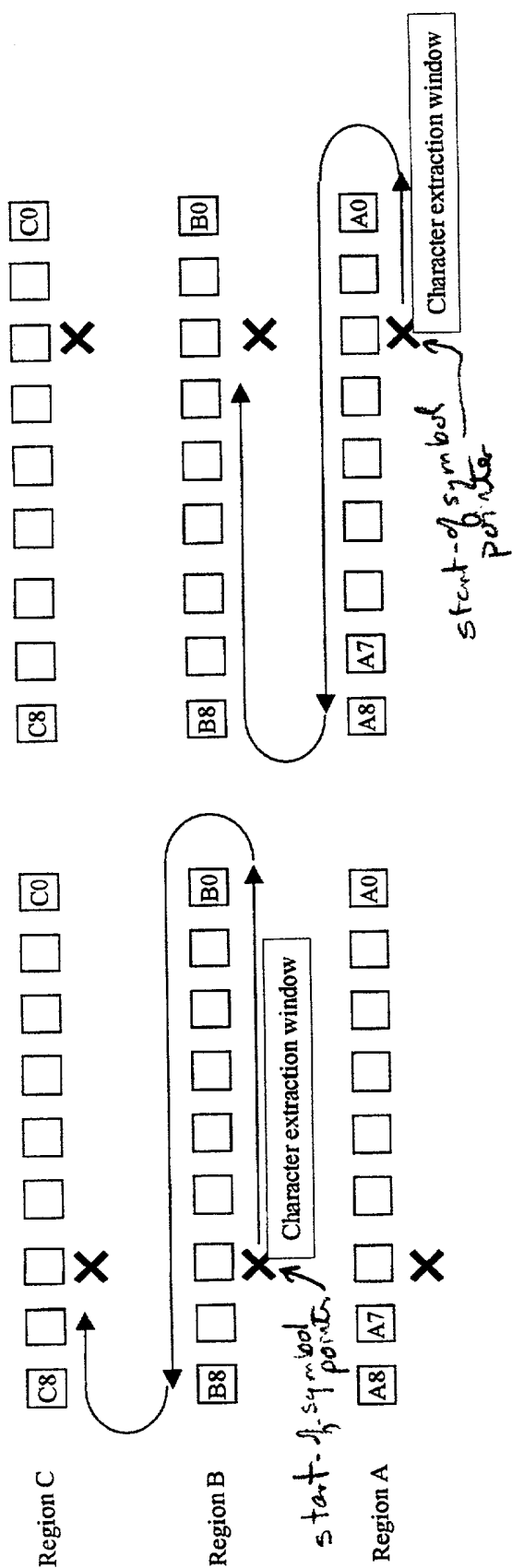
4302

Half line


$$\text{LD} = 1, \text{iHF} = 1, \text{iPTR} = "000000100"$$

SYNC. Mark

Fig. 3



LD = 1, iHF = 1, iPTR = "000000100"

LD = 1, iHF = 0, iPTR = "001000000"

844

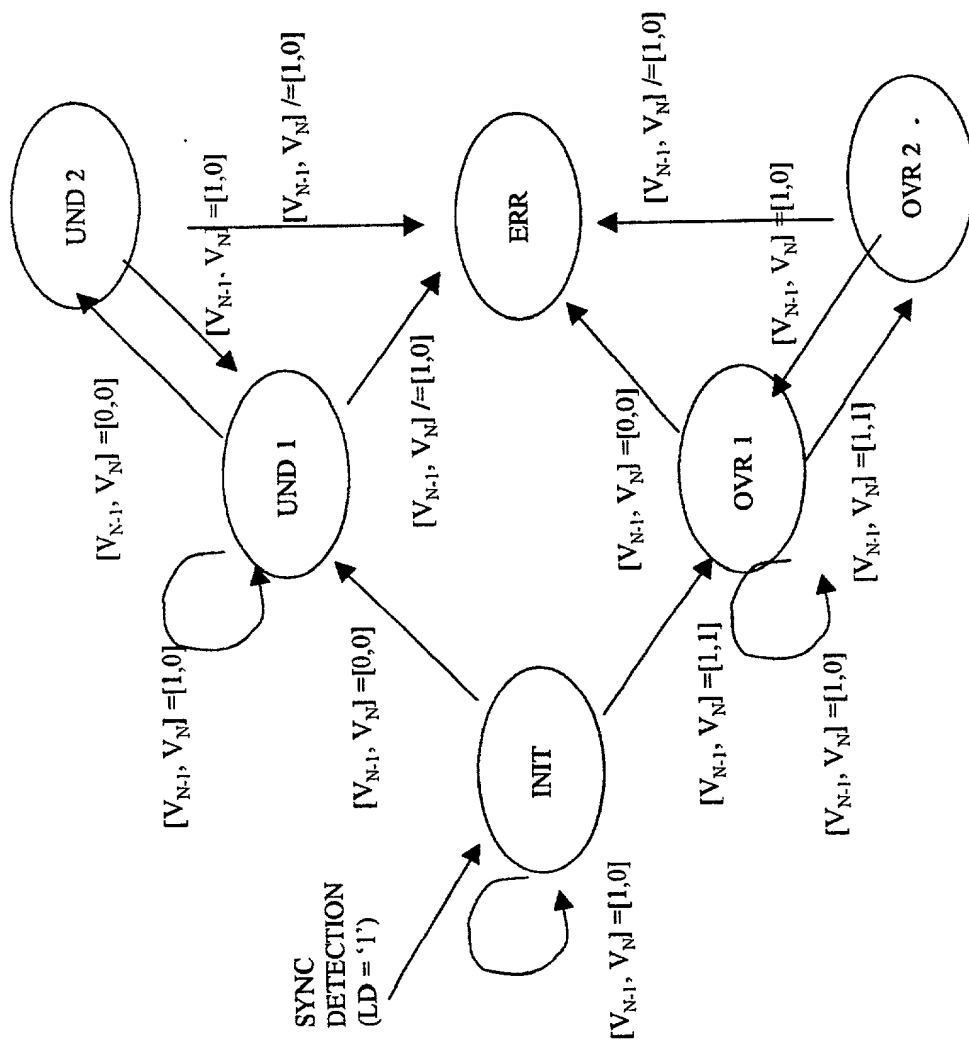


Fig 45

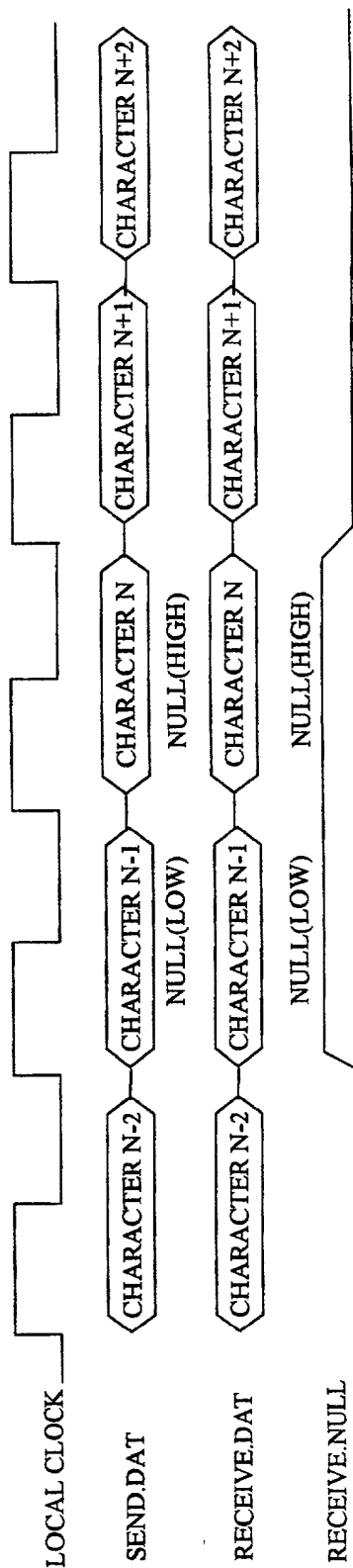
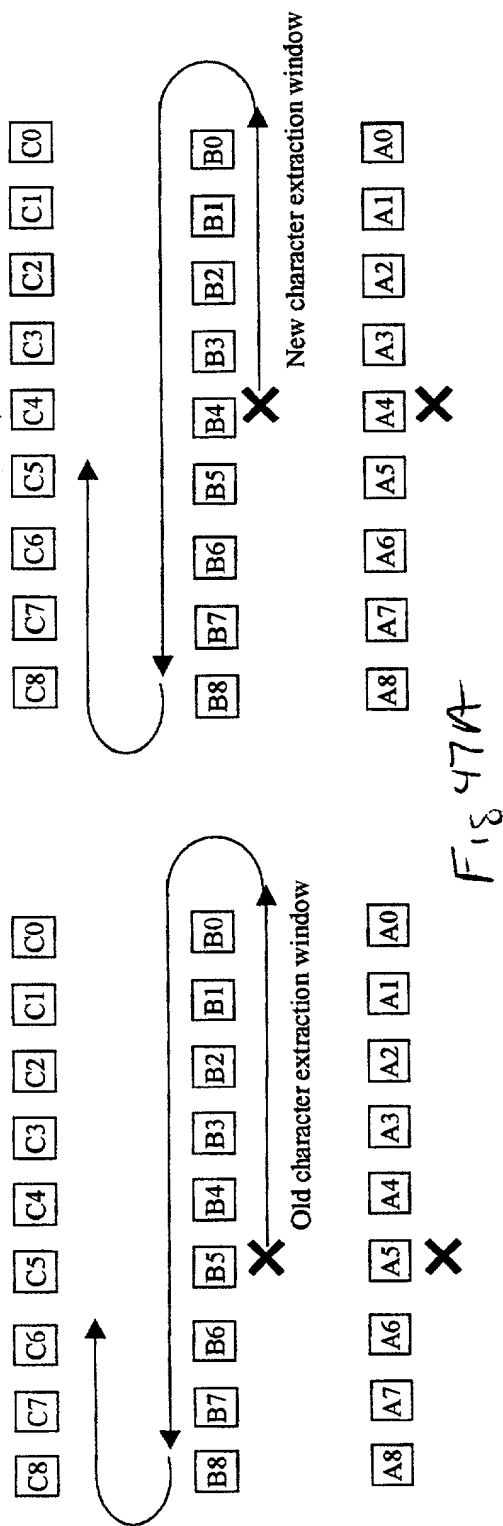
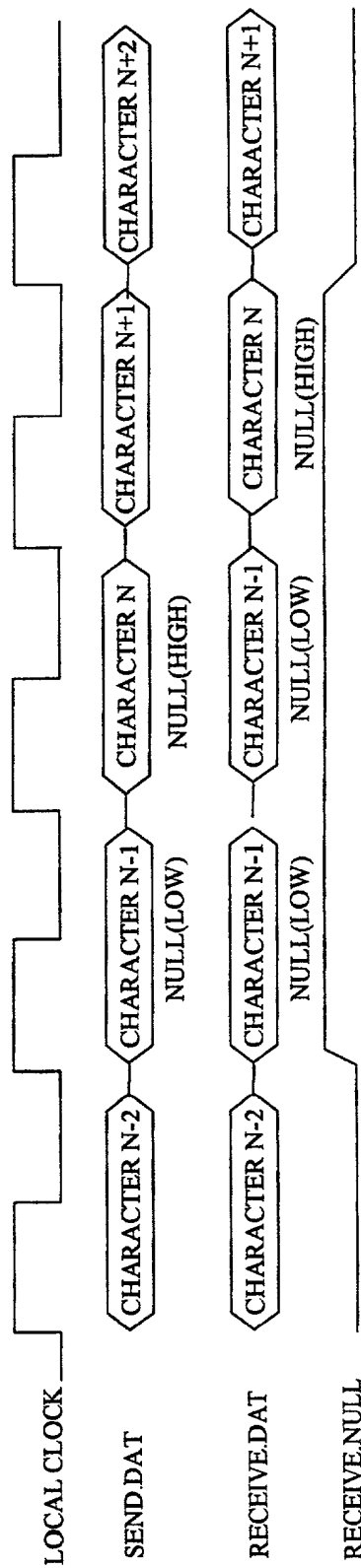
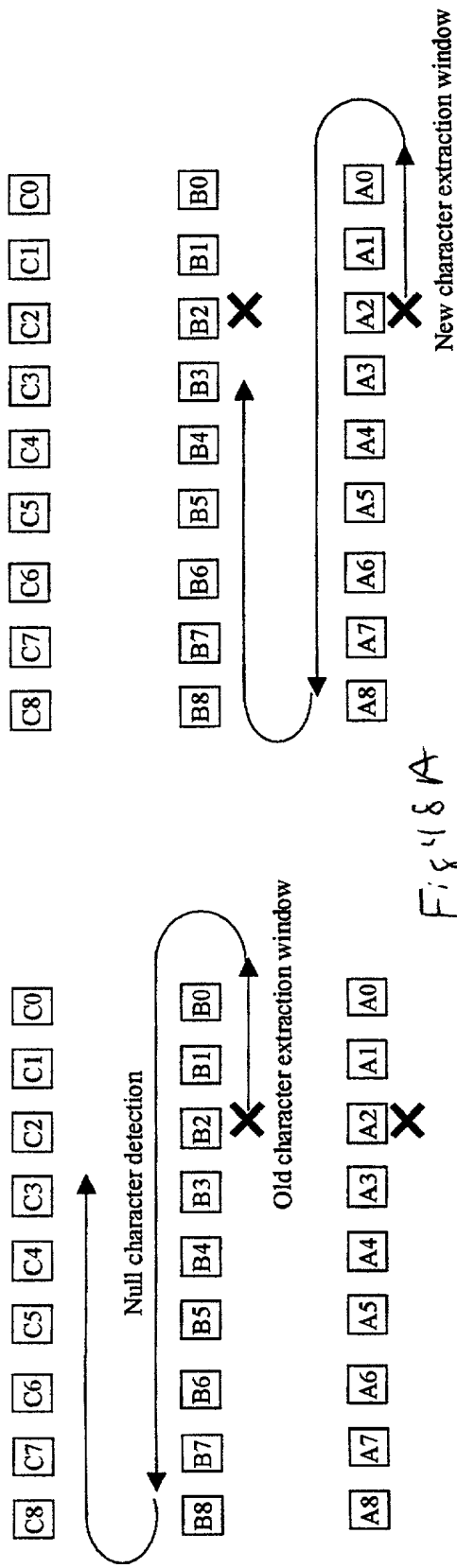


Fig 47B



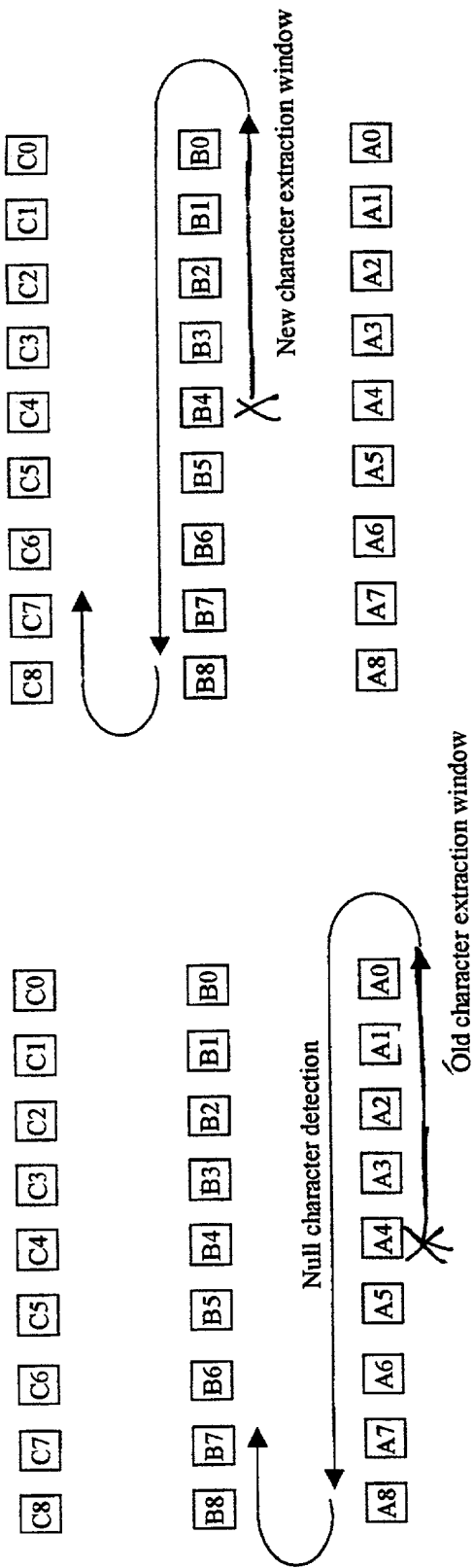


Fig. 49A

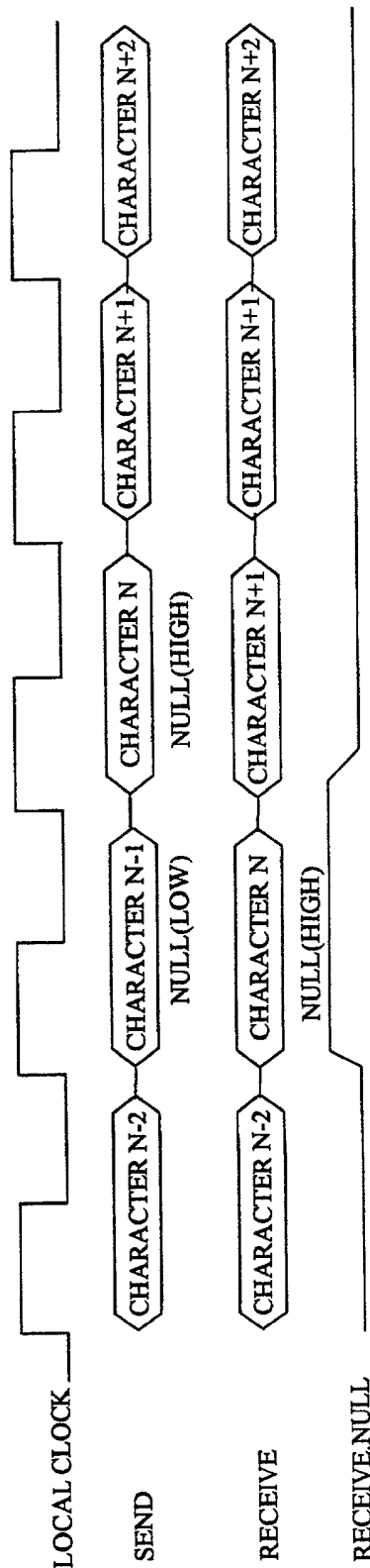


Fig 49B